

 Active

- L14: (4149) 365/222

L15: (6016) 365/230.03

☛ L16: (2076) 365/227

L17: (480486) resto

- 18: (13801) detect\$4 with memory with access\$4

19: (877) 17 same 18

120: (96484) (bank or)

121: (24) 19 same 20

126: (176) 18 same 2

127: (93) 17 and 26

128 (69) 27 not 21

129: (1815) (bank c

130: (104) (bank or block) with access\$4 with (restore\$4 or refresh\$4 or rewrite\$4 or recharge\$4 or

- 131: (25) 18 and 30

132: (79) 30 not (3)

135: (21) *5266231*

L36: (14) 28 and 36

✓ L48: (2061) 17 and 35

- 148: (2961) 17 same 20

149: (4/8) 48 and 14

150: (510) 48 and 15

L51: (81) 48 and 16

L53: (63) 18 and 49

L54: (66) 18 and 50

L56: (1276) 48 and 3

L58: (150) 18 and 56

L65:

 Failed

 Saved

 PDF icon
 Print icon
 Share icon
 Close icon
 Zoom icon
 Search icon
 Home icon
 Back icon
 Forward icon
 Refresh icon
 Print icon
 Close icon
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 Search icon
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 Refresh icon
 Print icon
 Close icon

	U	1	2	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla	Inventor	S	C	P	E
1	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 4914630 A	19900403	12	Refresh arrangement in a block divided memory including a plural	365/189.04	365/189.12; 365/222		Fujishima, Kazuyasu et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 4933907 A	19900612	32	Dynamic random access memory device and operating method there	365/222	365/230.03		Kumanoya, Masaki et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5796669 A	19980818	24	Synchronous semiconductor memory device	365/222	365/230.03; 365/233		Araki, Takashi et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6079023 A	20000620	9	Multi-bank memory devices having common standby voltage generate	713/300	711/106; 711/15		Yoon, Sei-Seung et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6226709 B1	20010501	14	Memory refresh control system	711/106	711/101; 711/104		Goodwin, Paul M. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6233192 B1	20010515	15	Semiconductor memory device	365/222			Tanaka, Hidehiko	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6680875 B2	20040120	36	Semiconductor device, such as a synchronous DRAM, including a co	365/233	365/226; 365/227		Horiguchi, Masashi et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6704234 B2	20040309	27	Semiconductor device, refreshing method thereof, memory system, a	365/222	365/230.03; 365/233		Mizugaki, Koichi	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

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Ready

Start Win... Clie... eD... IEA... 7... 106... 106... 106... 106... 106... 106... 106... Inb...

NUM

7:30 PM